Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (Currently Amended) A method of processing errors in a computer system, <u>having at least one processor, memory, and a bus coupled between the memory and the processor,</u> comprising ~~the steps of~~:

        identifying, <u>by a service processor,</u> failed hardware <u>of the computer system</u>;

        identifying, <u>by the service processor, other</u> hardware affected by the failed hardware <u>within the computer system</u>;

        <u>deconfiguring the failed hardware and the other hardware affected by the failed hardware; and</u>

        rebooting the computer system without running a diagnostic on the failed hardware.

2.   (Currently Amended) The method of claim 1, <u>wherein the deconfiguring and rebooting steps are performed by the service processor.</u> ~~further comprising the step of:~~

        ~~deconfiguring the failed hardware and the hardware affected by the failed hardware.~~

3.   (Canceled)

4.   (Original) The method of claim 1, wherein the step of deconfiguring includes activating at least one switch of circuitry of the computer system such that the failed hardware is excluded from the computer system.

5.   (Currently Amended) The method of claim 1, wherein the ~~failed hardware is identified~~ <u>service processor identifies the failed hardware</u> in a table <u>entry in a second memory</u> indicating [[it]] <u>that the failed hardware</u> has an error, and wherein the hardware affected by the failed hardware is <u>further</u> identified in the table indicating it is associated with the failed hardware.

6.   (Currently Amended) A computer system, <u>having at least one processor, memory, and a bus coupled between the memory and the processor</u>, comprising:

        a plurality of hardware units connected to the computer system by [[a]] <u>the</u> bus;

        a service processor having firmware;

        wherein when a first hardware unit of the plurality experiences an error, the first hardware unit is disconnected from the bus; and

wherein the computer system is restarted without running a first diagnostic associated with the first hardware unit.

7.   (Original) The computer system of claim 6, wherein the firmware of the service processor activates switches in circuitry of the computer system to disconnect the first hardware unit from the bus.

8.   (Original) The computer system of claim 6, wherein a table is updated with information indicating the first hardware unit of the plurality has an error associated therewith.

9.   (Original) The computer system of claim 6, wherein a second hardware unit of the plurality that is affected by the error of the first hardware unit of the plurality is disconnected from the bus.

10.   (Currently Amended) A computer program product in a computer readable medium, comprising:
        a computer system having <u>at least one processor, memory, a bus coupled between the memory and the processor, and</u> a first hardware unit connected to the computer system;
        first instructions for detecting<u>, by a service processor,</u> when an error occurs associated with the first hardware unit <u>of the computer system</u>;
        second instructions for disconnecting the first hardware unit from a bus of the computer system;
        third instructions for indicating in a table <u>entry in a second memory</u> that the first hardware unit has an error associated therewith;<u> and</u>
        fourth instructions for, when the computer system is rebooted, rebooting the computer system without running a first diagnostic associated with the first hardware unit.

11.   (Original) The product of claim 10, wherein the second instructions are firmware associated with a service processor of the computer system.

12.   (Original) The product of claim 10, wherein a second hardware that is affected by removal of the first hardware unit is also disconnected from the bus of the computer system.

13.   (Original) The product of claim 10, wherein disconnecting the first hardware unit includes activating at least one switch of circuitry of the computer system such that the first hardware is excluded from the computer system.

14.   (Currently Amended) The product of claim 12, wherein ~~the first hardware is identified~~ the service processor identifies the first hardware unit in a table entry in a second memory indicating [[it]] that the first hardware unit has an error, and wherein the second hardware affected by the failed hardware is identified in the table indicating it is associated with the failed hardware.

15.   (New) The method of claim 1, wherein the failed hardware is at least one bus interface unit, and wherein an error causing failure of the bus interface unit is a bus interface error.

16.   (New) The method of claim 5, further comprising:
        responsive to the failed hardware no longer having the error, resuming operation by the failed hardware and other hardware associated with the failed hardware.

17.   (New) The computer system of claim 6, wherein the first hardware unit is at least one bus interface unit, and wherein an error causing failure of the bus interface unit is a bus interface error.

18.   (New) The computer system of claim 6, further comprising:
        responsive to the first hardware unit no longer having the error, resuming operation by the first hardware unit and other hardware associated with the first hardware unit.

19.   (New) The computer program product of claim 10, wherein the first hardware unit is at least one bus interface unit, and wherein an error causing failure of the bus interface unit is a bus interface error.

20.   (New) The computer program product of claim 10, further comprising:
        responsive to the first hardware unit no longer having the error, resuming operation by the first hardware unit and other hardware associated with the first hardware unit.